FIG. 1
BACKGROUND ART

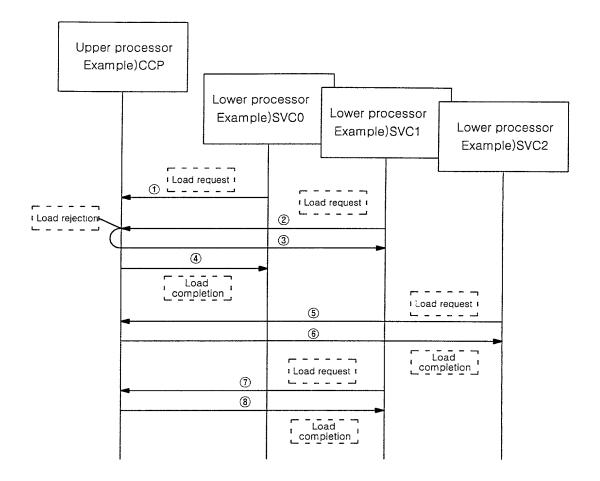


FIG. 2 BACKGROUND ART

Information contained in a memory of a upper processor

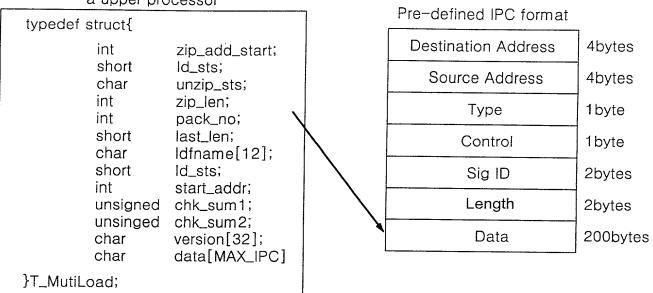


FIG. 3

Address of 4bytes

NA
ВА
CA
SA

NA(Node Address): Processor element ID

BA(BHIU Address): Processor ID CA(Cinu Address): Network group ID 1 SA(Slot Address): Network group ID 2

FIG. 4

